

Dr. C. Sreekanth

Assoc Professor & HOD

College of Engineering Muttathara,
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PROFESSIONAL SYNOPSIS

- ❖ A dynamic professional with 16+ years of experience
- ❖ A proactive team leader with abilities in training and motivating cross cultural teams
- ❖ Ability to quickly master new concepts, earn new technologies, enhancing the existing systems, equipment and software as well as capable of working in-group & independently with good communication skill

CORE COMPETENCIES

- ❖ Digital Control of Power Electronics Systems
- ❖ FPGA based PWM Algorithm development for Power Electronics Drive Systems
- ❖ Design and Analysis of Algorithms in FPGA

POSITIONS HELD

- ❖ Registered Research Supervisor in KTU
- ❖ Doctoral Committee member in KTU
- ❖ Expert Visit Committee member of KTU
- ❖ Board of Studies Member in other Universities
- ❖ Conducted and Resourced various Faculty training programs
- ❖ Chaired various IEEE Conferences.
- ❖ Coordinated NAAC and NBA accreditation process.
- ❖ IQAC Coordinator

ACADEMIC QUALIFICATIONS

- ❖ Doctorate (**Ph.D.**) on thesis titled “FPGA Based Implementation of Novel PWM Strategies for Voltage Source Inverters”, from Anna University, Chennai, India in 2013.
- ❖ Master Degree (**M.E**) with a specialization in Process Control and Instrumentation, from Annamalai University, Chidambaram, India – 2004.
- ❖ Bachelor Degree (**B.E**) with specialization in Instrumentation and Control Engineering, from Arulmigu Kalasalingam College of Engineering in 2001.

CAREER

- ❖ Assistant Professor in Department of Electronics and Instrumentation Engineering, Noorul Islam University, Kumaracoil, from 2005 to June 2014.
- ❖ Associate Professor / H.O.D in Department of Electrical and Electronics Engineering, Dr.Sivanthi Aditanar College of Engineering, Tiruchendur from June 2014 to December 2015.
- ❖ Associate Professor / H.O.D in Department of Electrical and Electronics Engineering, College of Engineering Vadakara (CAPE, Est. by Govt. of Kerala) from January 2016 to December 2017.
- ❖ Associate Professor / H.O.D in Department of Electrical and Electronics Engineering, College of Engineering Trikaripur (CAPE, Est. by Govt. of Kerala) from January 2018 to December 2018.
- ❖ Associate Professor / H.O.D in Department of Electrical and Electronics Engineering, College of Engineering Vadakara (CAPE, Est. by Govt. of Kerala) from January 2019 to November 2019.
- ❖ Associate Professor in EEE & Principal In-Charge, College of Engineering Vadakara (CAPE, Est. by Govt. of Kerala) from December 2019 to August 2020
- ❖ Associate Professor in EEE & Principal In-Charge, College of Engineering Muttathara (CAPE, Est. by Govt. of Kerala) from December 2020 to May 2023

PUBLICATIONS

International Journals:

1. Gokul Raghavendra Srinivasan , Vijayalakshmi Shankar , Sreekanth Chandra Sekharan. "Influence of fatty acid composition on process optimization and characteristics assessment of biodiesel produced from waste animal fat", Energy Sources, Part A: Recovery, Utilization, and Environmental Effects, DOI: 10.1080/15567036.2020.1771477
2. Sreekanth C. and Moni R.S. "FPGA Based Implementation of Amplitude Modulated Triangular Carrier PWM",Przeglad Elektrotechniczny (Electrical Review), Vol. 07, pp. 41-44, 2013.
3. Sreekanth C. and Moni R.S. "A Novel FPGA Based PWM Active Power Filter for Harmonics Elimination in Power System", International Journal of Electrical Engineering, Vol. 5, No.7, pp. 853-862, 2012.

International Conferences:

1. Sreekanth, C. and Moni, R.S. "FPGA Implementation of simplified AMTC-PWM Algorithm for a Single Phase Voltage Source Inverter", IEEE International Conference on Information and Communication Technologies (ICT 2013).
2. Sreekanth, C. and Moni, R.S. "Dynamically Reconfigurable PWM Controller for Single Phase Rectifiers", IEEE International Conference on Circuit, Power and Computing Technologies (ICCPCT 2013), pp. 258-264
3. Sreekanth, C. and Moni, R.S. "FPGA Implementation of Current mode Active Clamp ZVS DC-DC Converters", Proc. of International Conference on Recent Advances in Mech. Engineering (ICRAME 2010), pp. 688-696, 2010.

PERSONAL DOSSIER

Date of Birth : November 15th 1976
Sex : Male
Indian Marital Status : Married

Present Address:

Sreevisakh, CRA-91, TC 2/1485
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